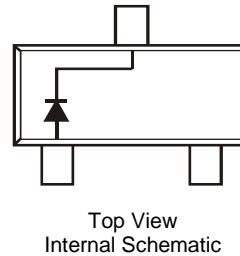
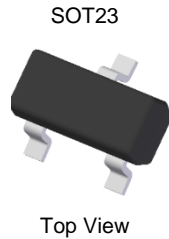


Features

- Fast Switching Speed
- Surface-Mount Package Ideally Suited for Automated Insertion
- For General Purpose Switching Applications
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Notes 3 & 4)**
- **The BAS16Q is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities. <https://www.diodes.com/quality/product-definitions/>**

Mechanical Data

- Package: SOT23
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish Annealed over Alloy 42 Leadframe (Lead Free Plating). Solderable per MIL-STD-202, Method 208
- Polarity: See Diagram
- Weight: 0.008 grams (Approximate)

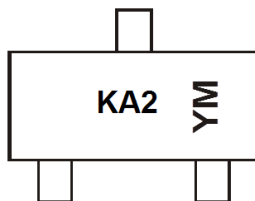


Ordering Information (Note 5)

Orderable Part Number	Package	Packing	
		Qty.	Carrier
BAS16-7-F	SOT23	3,000	Tape & Reel
BAS16-13-F	SOT23	10,000	Tape & Reel
BAS16Q-7-F	SOT23	3,000	Tape & Reel
BAS16Q-13-F	SOT23	10,000	Tape & Reel
MMBD4148-7-F	SOT23	3,000	Tape & Reel
MMBD4148-13-F	SOT23	10,000	Tape & Reel
MMBD914-7-F	SOT23	3,000	Tape & Reel
MMBD914-13-F	SOT23	10,000	Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
 2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen and Antimony free, "Green" and Lead-Free.
 3. Halogen and Antimony free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. Product manufactured with Date Code W9 (week 39, 2009) and newer are built with Green Molding Compound. Product manufactured prior to Date Code W9 are built with Non-Green Molding Compound and may contain Halogens or Sb₂O₃ Fire Retardants.
 5. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

Marking Information



KA2, KA2 = Product Type Marking Code
 YM = Date Code Marking
 Y = Year (ex: L = 2024)
 M = Month (ex: 9 = September)
 A Bar around the Date Code Marking Denotes Assembly Site

Date Code Key

Year	2002	-	2024	2025	2026	2027	2028	2029	2030	2031	2032	2033
Code	N	-	L	M	N	P	R	S	T	U	V	W

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Non-Repetitive Peak Reverse Voltage	V _{RM}	100	V
Peak Repetitive Reverse Voltage	V _{RRM}	75	V
Working Peak Reverse Voltage	V _{RWM}		
DC Blocking Voltage	V _R		
RMS Reverse Voltage	V _{R(RMS)}	53	V
Forward Continuous Current (Note 6)	I _{FM}	300	mA
Non-Repetitive Peak Forward Surge Current	I _{FSM}	@ t = 1.0μs 2.0	A
		@ t = 1.0s 1.0	

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 6)	P _D	350	mW
Thermal Resistance Junction to Ambient Air (Note 6)	R _{θJA}	357	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 7)	V _{(BR)R}	75	—	V	I _R = 100μA
Forward Voltage	V _F	—	0.715 0.855 1.0 1.25	V	I _F = 1.0mA I _F = 10mA I _F = 50mA I _F = 150mA
Leakage Current (Note 7)	I _R	—	1.0 50 30 25	μA μA μA nA	V _R = 75V V _R = 75V, T _J = +150°C V _R = 25V, T _J = +150°C V _R = 20V
Total Capacitance	C _T	—	2.0	pF	V _R = 0, f = 1.0MHz
Reverse Recovery Time	t _{RR}	—	4.0	ns	I _F = I _R = 10mA, I _{RR} = 0.1 x I _R , R _L = 100Ω

Notes: 6. Device mounted on FR-4 substrate PC board with 1 inch square, 2oz copper pad layout.
7. Short duration pulse test used to minimize self-heating effect.

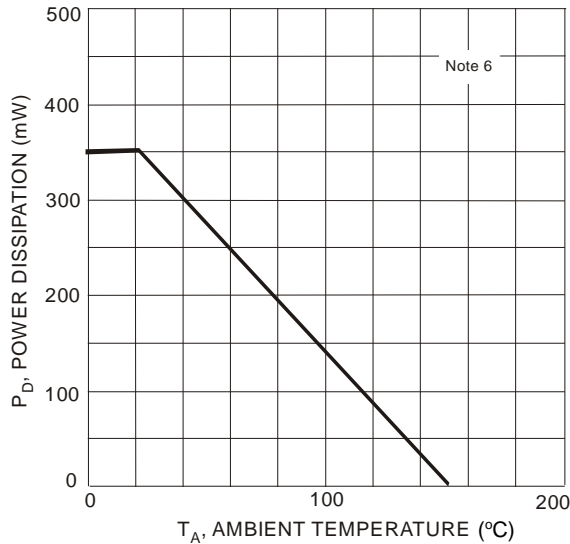


Fig. 1 Power Derating Curve

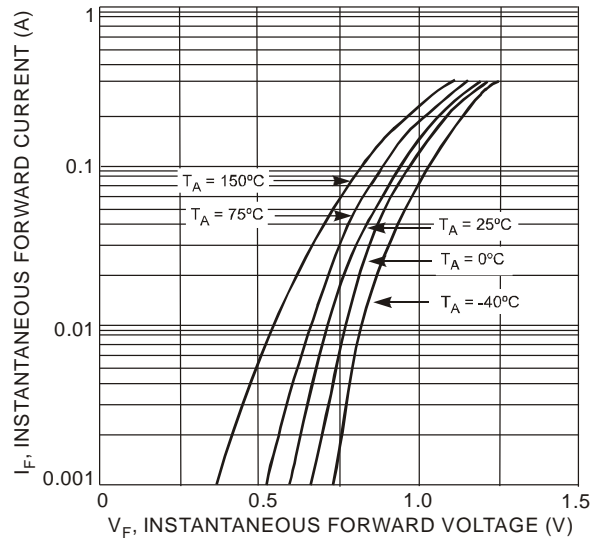


Fig. 2 Typical Forward Characteristics

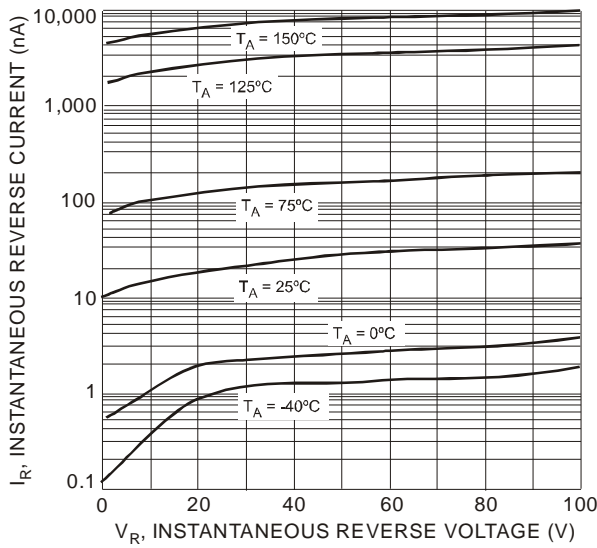


Fig. 3 Typical Reverse Characteristics

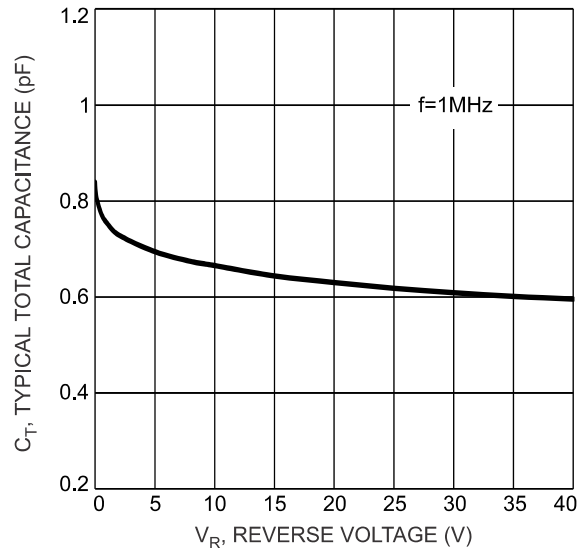
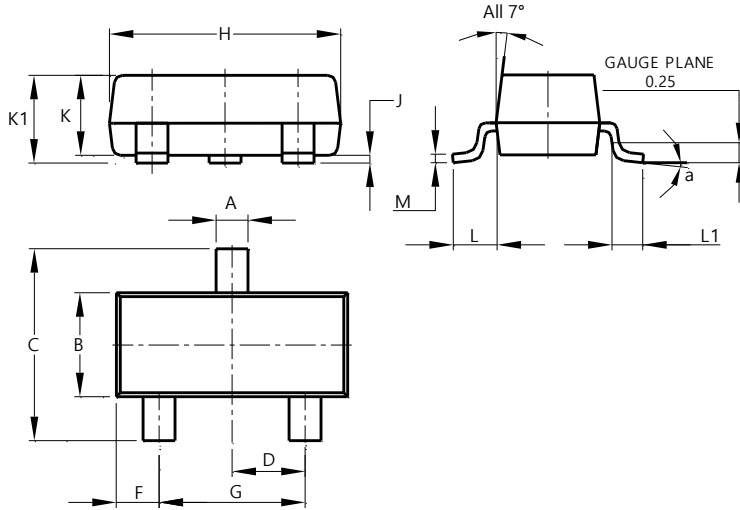


Fig. 4 Typical Total Capacitance

Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SOT23

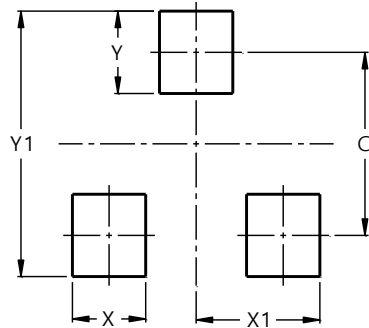


SOT23			
Dim	Min	Max	Typ
A	0.37	0.51	0.40
B	1.20	1.40	1.30
C	2.30	2.50	2.40
D	0.89	1.03	0.915
F	0.45	0.60	0.535
G	1.78	2.05	1.83
H	2.80	3.00	2.90
J	0.013	0.10	0.05
K	0.890	1.00	0.975
K1	0.903	1.10	1.025
L	0.45	0.61	0.55
L1	0.25	0.55	0.40
M	0.085	0.150	0.110
a	0°	8°	--
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SOT23



Dimensions	Value (in mm)
C	2.0
X	0.8
X1	1.35
Y	0.9
Y1	2.9

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